

Title: FLEXIBLE CACHING OF TRANSLATED CODE UNDER EMULATION

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FIG. 1

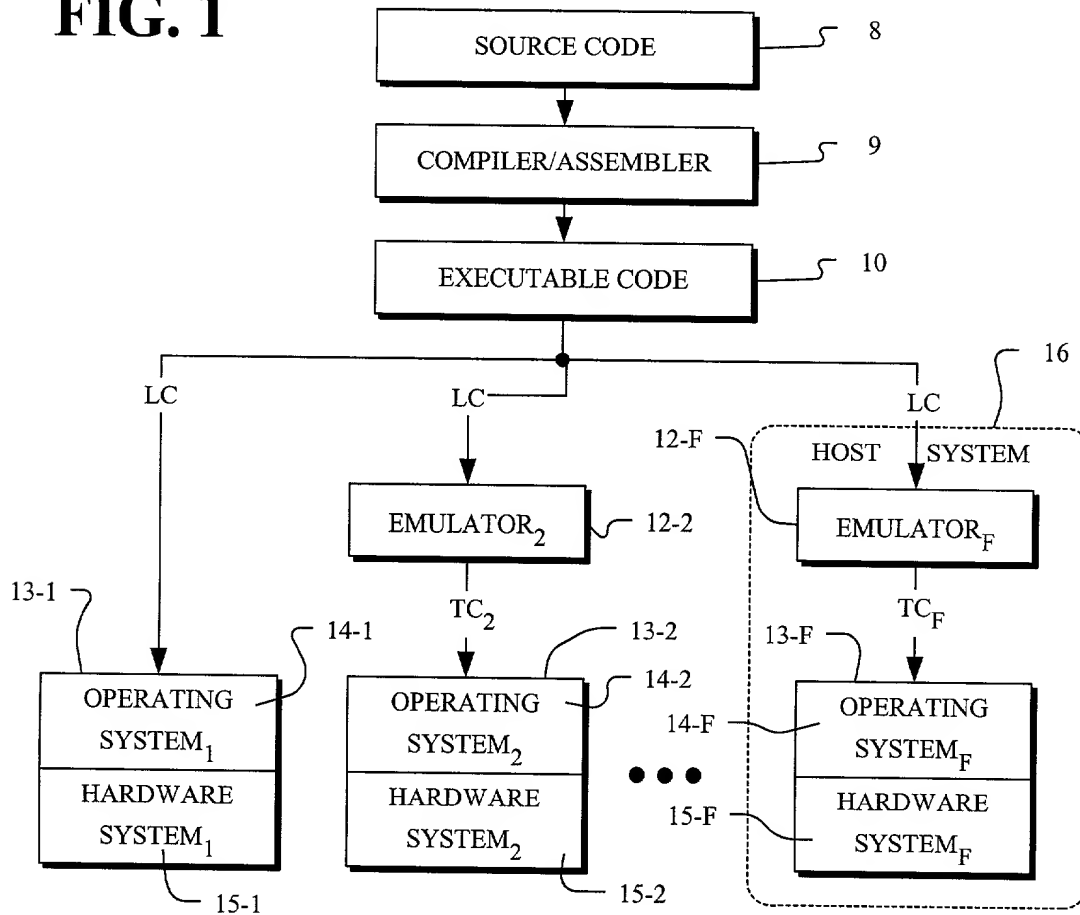
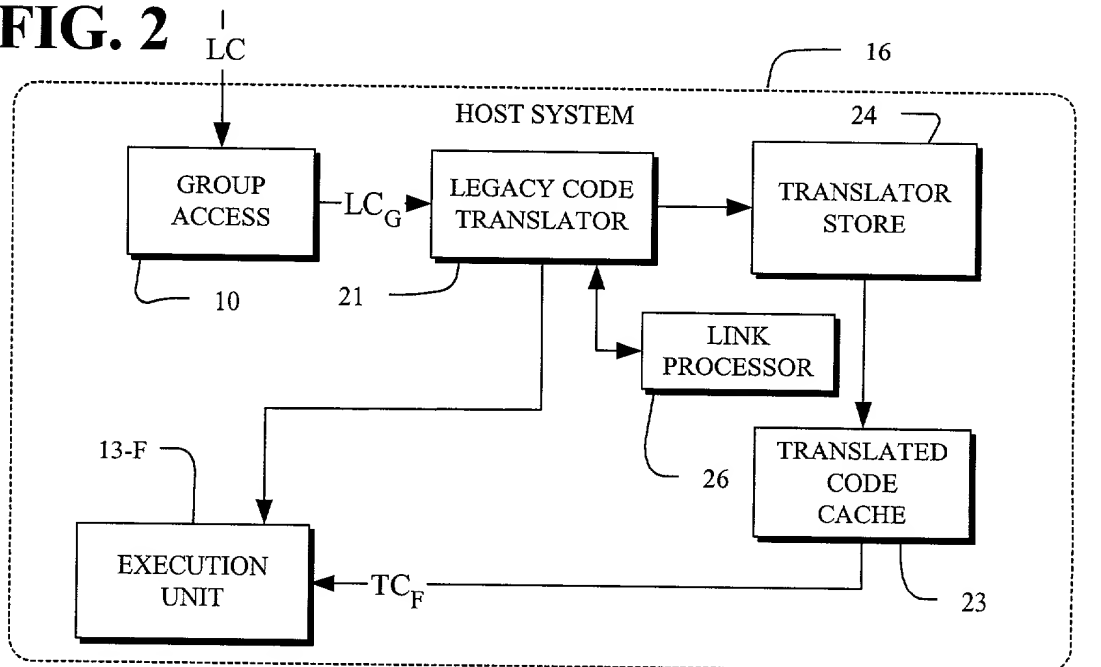


FIG. 2



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LEGACY CODE (CISC)

100	START	BALR	B1,R0
102	BASE	LM	R1,R2,DATA1
106		MVC	DATA1, DATA3
10C		A	R1, DATA2
110		H	
110		SRA	R1,1
114		SH	R2,DATA2
118		AR	R1,R2
11A		BC	TARGET
120	DATA1	DC	X'005390BC'
		DC	X'09C20004'
128	DATA2	DC	X'0009'
12A	DATA3	DC	X'800039AF'

FIG. 3



100	START	BALR	B1, R0
102	BASE	LM	R1, R2, DATA1
106		MVC	DATA1, DATA3
10C		A	R1, DATA2
		H	

3_C-10



TRANSLATED CODE (RISC)

BALR	MOV	B1	BASE
LM	AD	A1	B1, DATA1 - BASE
	D	R1	[A1]
	LD4	A1	A1,4
	D	R2	[A1]
	LD4	A1	B1, DATA1 - BASE
MVC	AD	A2	B1, DATA3 - BASE
	D	T1	[A2]
	ST4	T1	[A1]
A	AD	A1	B1, DATA2 - BASE
H	D	T1	[A1]
	LD2	R1	R1, T1
	D		
	B		XFER_SEQUENTIAL

3_R-10

3_C-11

110		SRA	R1,1
114		SH	R2, DATA2
118		AR	R1, R2
11A		BC	TARGET



120	DATA1	DC	X'005390BC'
		DC	X'09C20004'
128	DATA2	DC	X'0009'
12A	DATA3	DC	X'800039AF'

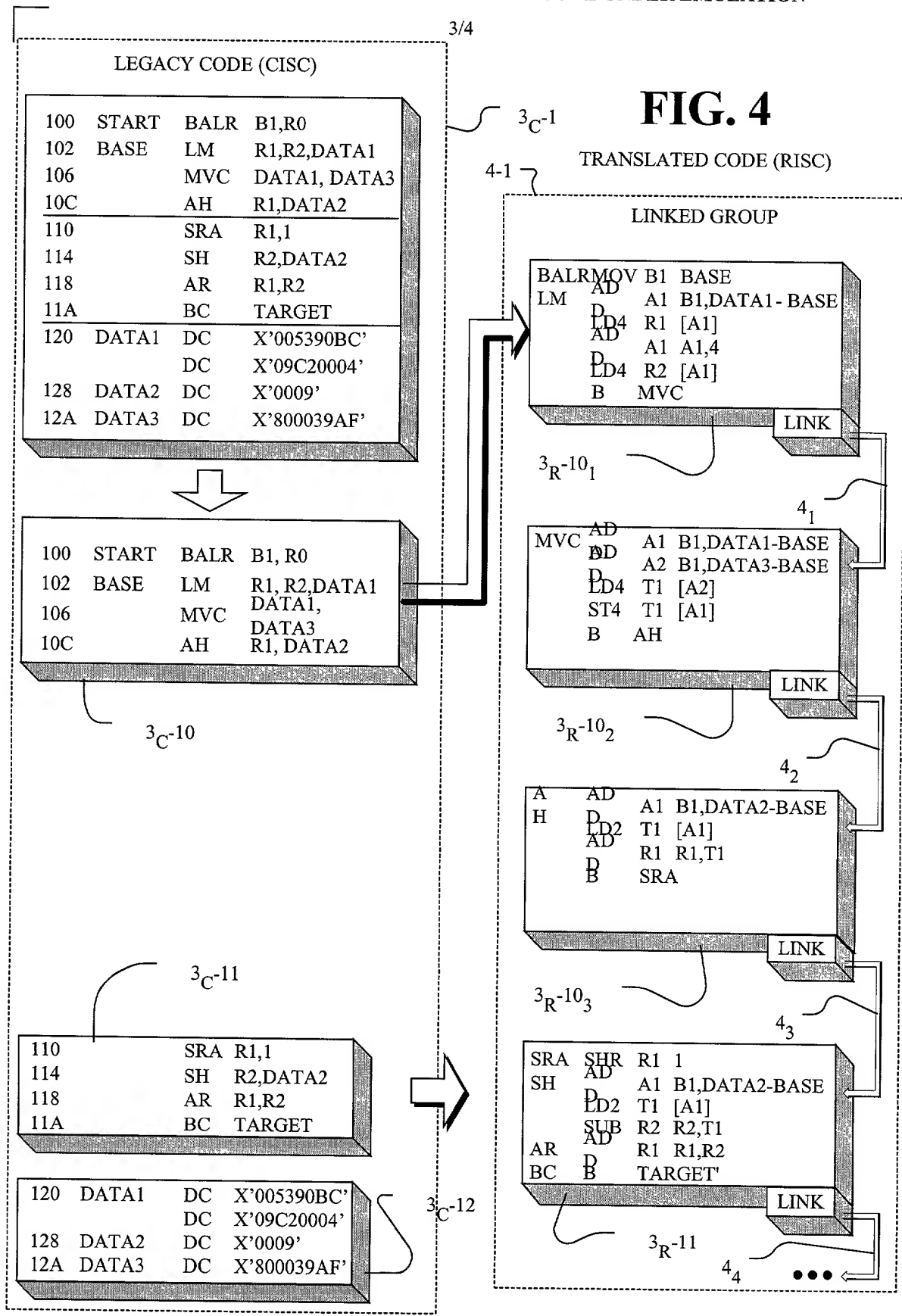
3_C-12

3_R-11

SRA	SHR	R1	1
SH	AD	A1	B1, DATA2 - BASE
	D	T1	[A1]
	LD2	R2	R2, T1
	SUB	R1	R1, R2
AR	AD	A1	B1, TARGET - BASE
BC	D		
	B		XFER_BRANCH

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FIG. 4



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FIG. 5

